Complementary Silicon Plastic Power Transistors

These devices are designed for use as high-frequency drivers in audio amplifiers.

Features

- High Current Gain Bandwidth Product
- TO-220 Compact Package
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CEO}	120 150	Vdc
Collector-Base Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CB}	120 150	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	Ic	8.0	Adc
Collector Current – Peak	I _{CM}	16	Adc
Base Current	Ι _Β	2.0	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	50 0.40	W W/°C
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	2.0 0.016	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

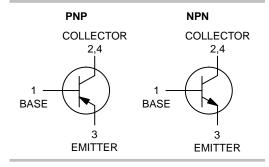
Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W



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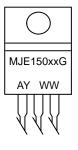
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8 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 120-150 VOLTS, 50 WATTS





MARKING DIAGRAM



MJE150xx = Device Code x = 28, 29, 30, or 31

A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	·			
Collector–Emitter Sustaining Voltage (Note 1) ($I_C = 10 \text{ mAdc}$, $I_B = 0$) MJE15028, MJE15029 MJE15030, MJE15031	V _{CEO(sus)}	120 150	- -	Vdc
Collector Cutoff Current $(V_{CE} = 120 \text{ Vdc}, I_B = 0)$ MJE15028, MJE15029 $(V_{CE} = 150 \text{ Vdc}, I_B = 0)$ MJE15030, MJE15031	I _{CEO}	-	0.1 0.1	mAdc
Collector Cutoff Current $(V_{CB} = 120 \text{ Vdc}, I_E = 0)$ $MJE15028, MJE15029$ $(V_{CB} = 150 \text{ Vdc}, I_E = 0)$ $MJE15030, MJE15031$	I _{CBO}	-	10 10	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	10	μAdc
ON CHARACTERISTICS (Note 1)				
DC Current Gain ($I_C = 0.1 \text{ Adc}$, $V_{CE} = 2.0 \text{ Vdc}$) ($I_C = 2.0 \text{ Adc}$, $V_{CE} = 2.0 \text{ Vdc}$) ($I_C = 3.0 \text{ Adc}$, $V_{CE} = 2.0 \text{ Vdc}$) ($I_C = 4.0 \text{ Adc}$, $V_{CE} = 2.0 \text{ Vdc}$)	h _{FE}	40 40 40 20	- - - -	_
DC Current Gain Linearity (V _{CE} From 2.0 V to 20 V, I _C From 0.1 A to 3 A) (NPN to PNP)	h _{FE}	Typ 2 3		
Collector–Emitter Saturation Voltage (I _C = 1.0 Adc, I _B = 0.1 Adc)	V _{CE(sat)}	-	0.5	Vdc
Base–Emitter On Voltage (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	-	1.0	Vdc
DYNAMIC CHARACTERISTICS	<u> </u>			
Current Gain – Bandwidth Product (Note 2) (I _C = 500 mAdc, V _{CE} = 10 Vdc, f _{test} = 10 MHz)	f⊤	30	-	MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

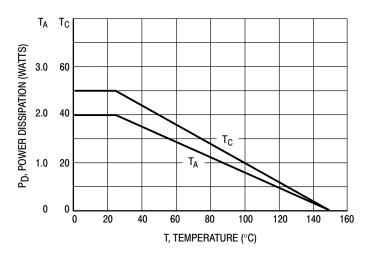


Figure 1. Power Derating

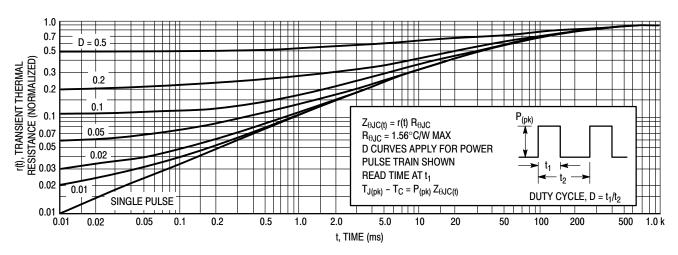


Figure 2. Thermal Response

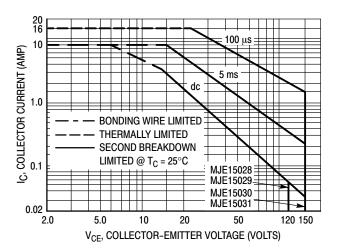


Figure 3. Forward Bias Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation then the curves indicate.

The data of Figures 3 and 4 is based on $T_{J(pk)} = 150^{\circ} C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ} C$. $T_{J(pk)}$ may be calculated from the data in Figure 2. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

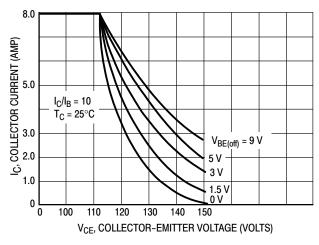


Figure 4. Reverse–Bias Switching Safe Operating Area

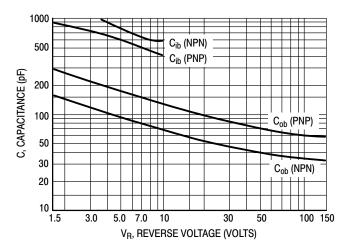
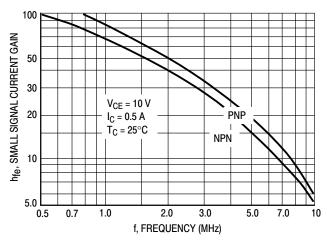


Figure 5. Capacitances

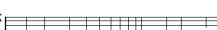


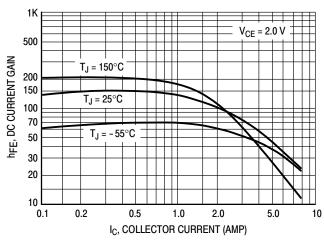
100 f_T, CURRENT GAIN-BANDWIDTH PRODUCT (MHz) 90 (PNP) (NPN) 60 50 20 10 0 L 0.1 0.2 0.5 2.0 5.0 1.0 10 IC, COLLECTOR CURRENT (AMP)

Figure 6. Small-Signal Current Gain

NPN — MJE15028 MJE15030

Figure 7. Current Gain-Bandwidth Product





PNP — MJE15029 MJE15031

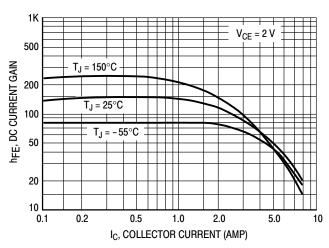
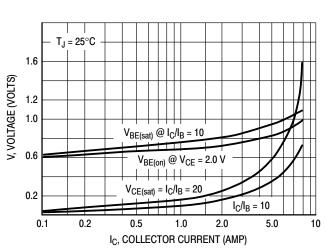
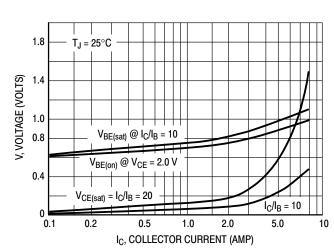


Figure 8. DC Current Gain



NPN



PNP

Figure 9. "On" Voltage

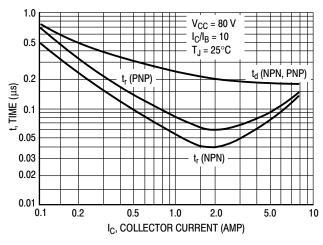


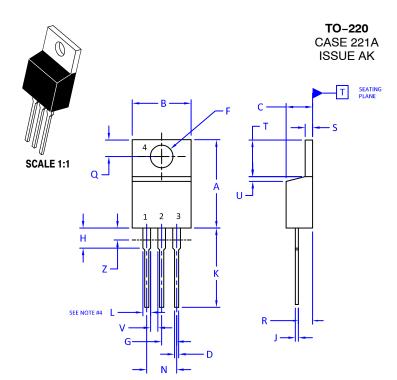
Figure 10. Turn-On Times

Figure 11. Turn-Off Times

ORDERING INFORMATION

Device	Package	Shipping
MJE15028G	TO-220 (Pb-Free)	50 Units / Rail
MJE15029G	TO-220 (Pb-Free)	50 Units / Rail
MJE15030G	TO-220 (Pb-Free)	50 Units / Rail
MJE15031G	TO-220 (Pb-Free)	50 Units / Rail





DATE 13 JAN 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: INCHES
- 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

4. MAX WIDTH FOR F102 DEVICE = 1.35MM

	INCHES		MILLIMETERS	
DIM	MIN.	MAX.	MIN.	MAX.
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.60	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
К	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.41
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045		1.15	
Z		0.080		2.04

STYLE 1: PIN 1. 2. 3. 4.	COLLECTOR EMITTER	STYLE 2: PIN 1. 2. 3. 4.	COLLECTOR	STYLE 3: PIN 1. 2. 3. 4.	ANODE	2. 3.	MAIN TERMINAL 1 MAIN TERMINAL 2 GATE MAIN TERMINAL 2
STYLE 5: PIN 1. 2. 3. 4.	DRAIN SOURCE	STYLE 6: PIN 1. 2. 3. 4.	CATHODE ANODE	STYLE 7: PIN 1. 2. 3. 4.	ANODE	2. 3.	CATHODE ANODE EXTERNAL TRIP/DELAY ANODE
STYLE 9: PIN 1. 2. 3. 4.			GATE SOURCE DRAIN SOURCE	STYLE 11: PIN 1. 2. 3. 4.		STYLE 12: PIN 1. 2. 3. 4.	MAIN TERMINAL 1 MAIN TERMINAL 2 GATE NOT CONNECTED

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